

	Abbreviation	Description		Abbreviation	Description
I	INT INIT I/O 1–8 IOB, IOG, IOR IP11	H Sync Interruption Used for Two Way Communication Data In/Out 1–8 B, G, R Current Output V Sync Input	P	PAP/INK PCDEC P CHK P CHKL P CHKS	Paper/Ink PC Enable Paper Mark Sensor Output (Mark Portion: H/No Mark: L) Paper Check Sensor LED Paper Check Sensor Output
	IP0 IP2 IP3, IP4 IREF IRQ	EE PROM Data Input DSP Data Input Flag Head Motor Position Detection In/Out Reference Current Infra Red Remote Control Data		PCP PD0–7 PDEND PE LED PEND	Pedestal Clamp Pulse Picture Element Operation Data 6 CLK Period of SAMCK (H) Paper Ink LED Paper End Sensor Output (No Paper: H/Exist: L)
	IS IV 0–7	Issue Current DSP Picture Element Data Input		PENDL PH LD SW, P KEY +– PMD0, 1 PON H	Paper End Sensor LED Cassette In Switch (Cassette In: L/Non: H) Power Switch Key Scan Head Motor Power Source 0, 1 Power ON: H
				P OUT P OUTL P OUTS PPOS PRTDM	Paper Eject Sensor Output (No Paper: H/Exist: L) Paper Eject Sensor LED Paper Eject Sensor Output Paper Beginning/End Detection (No Paper: H/Exist: L) Digital Memory Element Data
J	JAMSW	Jamming Switch		PR/W PSH PSP0, 1, 2, 3 PTRBUSY PTRCLK	Parallel Read/Write Control Sheet Motor Power Source Stepping Motor Power Source 0, 1, 2, 3 Used for Two Way Communication Used for Two Way Communication
K	KEYD 0–3 KEYS 0–3	Key Data 0–3 Key Scan 0–3		PWM0, 1	Stepping Motor Drive 0, 1
L	LDFG LED 0–3 LSB	Gradation Control Flag LED Control 0–3 Lowest Bit	R	RAS RDRF RE ROP1 RRDY	Load Address Strobe Receive Register Full Read Enable Memory/Through Select Output Parallel Receive Ready
M	MEM C, Y MEM/THR MODAS, MODBS MODAL, MODBL MPX	Memory Chroma, Y Memory/Through Select Mode A Sensor, Mode B Sensor Mode A LED, Mode B LED Multiplex 3.4MHz		RT R/W RUNCPC RXD	Oscillation Time Constant Resistor Read/Write CPU Interruption Receive Data
	MRDB MRWB MSB MWRB	Micro Computer Read Signal Input Micro Computer Read/Write Signal Input Top Bit Micro Computer Write Signal Input			
N	NOPL NOP NOPS NTSC/YC	No Paper LED No Paper: H No Paper Sensor NTSC: H/YC: L			
O	OBFB ODEV OE OKS	DSP Data Output Flag ODD Field: H/EVEN Field: L Output Enable Memory Control Data Receivable Sign OK: H/Error: L	S	S-SW S0–S3 S0B, S1B, S2B, S3B SA00–SA12 SAMCK	S-VHS Cable Connection: L System Clock 0–3 System Clock Input S RAM Address Bus Output PD Sampling Clock
	OP0, 1 OP8 OP4 OP5 OP6	Sensor Select Signal Output Timing Pulse EEPROM Chip Select EEPROM Clock Output EEPROM Data Output		SBT1B SBSDO SBRDO SBT 0 SBI0, SBO0	OSD Serial Clock Data Input Data Output Operation Serial Clock Operation Serial Data Input/Output
	OP7 OP LED	NTSC Y/C Select Output Print LED		SBT SBO1B SBI1B S CLK SC	Memory Control Data Transfer Completion Sign OSD Serial Data OSD Chip Select Memory Control Serial Clock Sub Carrier
				SCSB SD SD0–7	S RAM Control Output Thermal Head Serial Output S RAM Data Bus I/O 0–7