

Pin No.	Port Name	Function	I/O	Pin No.	Port Name	Function	I/O
1	HDDT(4)	Thermal Head Data	O	61	PORT(23)	Not Used	I/O
2	HDDT(5)	Thermal Head Data	O	62	S0TXI	Not Used	O
3	HDDT(6)	Thermal Head Data	O	63	TM5O	Timer Out	O
4	HDDT(7)	Thermal Head Data	O	64	VCC(3.3V)	VCC(3.3V)	
5	HDDT(8)	Thermal Head Data	O	65	GND	GND	
6	HDDT(9)	Thermal Head Data	O	66	EIRQ(0)	External Request Input	I
7	HEADCK	Thermal Head Clock	O	67	EIRQ(1)	External Request Input	I
8	LATCH	Thermal Head Latch	O	68	EIRQ(2)	External Request Input	I
9	DG	Thermal Head Enable	O	69	EIRQ(3)	External Request Input	I
10	VCC(5V)	VCC(5V)		70	EIRQ(4)	External Request Input	
11	GND	GND		71	EIRQ(5)	External Request Input	I
12	VCC(3.3V)	VCC(3.3V)		72	EIRQ(6)	Not Used	I
13	GND	GND		73	EIRQ(7)	External Request Input	I
14	UXIN	OSC In(48MHz)	I	74	EIRQ(8)	Not Used	I
15	UXOUT	OSC Out(48MHz)	O	75	MMCMD	MMC Command	I/O
16	GND	GND		76	MMCLK	MMC Clock	O
17	ST	Not Used	I	77	MMDAT	MMC Data	I/O
18	SW	Not Used	I	78	GND	GND	
19	VCC(3.3V)	VCC(3.3V)		79	VCC(3.3V)	VCC(3.3V)	
20	DP	Not Used	I/O	80	MMCINT	MMC Interrupt	O
21	DM	Not Used	I/O	81	MMCVCTL	Not Used	O
22	GND	GND		82	SMRXRDY	Not Used	O
23	TP0	Not Used	I	83	SMTXRDY	Not Used	O
24	TP1	Not Used	I	84	PRTBSY	Not Used	O
25	SUSPEND	Not Used	O	85	PCRXRDY	Not Used	O
26	GND	GND		86	IRL0	Interrupt Code Out	O
27	VCC(3.3V)	VCC(3.3V)		87	IRL1	Interrupt Code Out	O
28	TXDPLS	Not Used	O	88	IRL2	Interrupt Code Out	O
29	TXDMNS	Not Used	O	89	IRL3	Interrupt Code Out	O
30	TXSE0	Not Used	O	90	DREQN(0)	DMA Request Out(USB)	O
31	TXENL	Not Used	O	91	DREQN(1)	DMA Request Out(USB)	O
32	XVER_DATA	Not Used	I	92	VCC(3.3V)	VCC(3.3V)	
33	VCC(3.3V)	VCC(3.3V)		93	GND	GND	
34	DPLS	Not Used	I	94	WAIT	Wait	O
35	DMNS	Not Used	I	95	DRAK(0)	DMA Request In(USB)	I
36	USBINT1	USB Request In	O	96	DRAK(1)	DMA Request In(USB)	I
37	USBINT2	USB Request In	O	97	DACKN(0)	DMA Request Acknowledge(USB)	I
38	VBUS	USB Bus Power In	I	98	DACKN(1)	DMA Request Acknowledge(USB)	I
39	TP2	Not Used	I	99	CKIO	Clock In	I
40	VCC(3.3V)	VCC(3.4V)		100	CSA	Chip Select	I
41	GND	GND		101	WRH	Write(Upper)	I
42	TM2	Not Used	I	102	WRL	Write(Lower)	I
43	RST	Reset	I	103	VCC(3.3V)	VCC(3.3V)	
44	TP3	Not Used	I	104	GND	GND	
45	TP4	Not Used	I	105	AGND	AGND	
46	PORT(10)	Not Used	I/O	106	VREF(3.3V)	VREF(3.3V)	I
47	PORT(11)	Not Used	I/O	107	AD(0)	A/D In	I
48	PORT(12)	Not Used	I/O	108	AD(1)	A/D In	I
49	PORT(13)	Not Used	I/O	109	AD(2)	A/D In	I
50	PORT(14)	Not Used	I/O	110	AD(3)	A/D In	I
51	VCC(3.3V)	VCC(3.3V)		111	AVCC(3.3V)	AVCC(3.3V)	
52	GND	GND		112	AD(4)	A/D In	I
53	PORT(15)	Not Used	I/O	113	AD(5)	A/D In	I
54	PORT(16)	Extension Port	I/O	114	AD(6)	A/D In	I
55	PORT(17)	Not Used	I/O	115	AD(7)	A/D In	I
56	PORT(18)	Not Used	I/O	116	AGND	AGND	
57	PORT(19)	Not Used	I/O	117	PGND	PGND	
58	PORT(20)	Extension Port	I/O	118	PVCC(3.3V)	PVCC(3.3V)	
59	PORT(21)	Extension Port	I/O	119	XIN	OSC In(8MHz)	I
60	PORT(22)	Extension Port	I/O	120	XOUT	OSC Out(8MHz)	O