A-LINE

FL12 F1H0J1050019 — C5 0.1, C7 0.1, C10 0.1, R26 0, C3 6V47
FL11 F1H0J1050019 — C11 0.1, C12 0.1, C13 0.1, C15 0.1, R24 0, C4 6V47
FL13 F1H0J1050019 — C6 0.1, C9 0.1
FL15 F1H0J1050019 — C21 0.1, C23 0.1, C24 0.1, R37 0, C19 6V47
FL14 F1H0J1050019 — C16 0.1, C18 0.1
FL4 F1H0J1050019 — R50 0, C33 1
FL3 F1H0J1050019 — C34 1
FL5 F1H0J1050019 — R42 47K

LX2 J0JBD0000032: D15 D14 D13 D12 → (A34) (A35) (A36) (A37)
LX3 J0JBD0000032: D11 D10 D9 D8 → (A38) (A39) (A40) (A41)
LX4 J0JBD0000032: D7 D6 D5 D4 → (A42) (A43) (A44) (A45)
LX5 J0JBD0000032: D3 D2 D1 D0 → (A46) (A47) (A48) (A49)
RX1 EXBV8VR00V: (A50) (A51) (A52) (A53) → DREQ1 DREQ0 DRAK1 DRAK0
LX11 J0JBD0000032: (A68) (A69) (A70) (A71)
LX6 J0JBD0000032: A1 A2 A3 A4 → (A72) (A73) (A74) (A75)
LX7 J0JBD0000032: A5 A6 A7 A8 → (A76) (A77) (A78) (A79)
LX8 J0JBD0000032: A9 A10 A11 A12 → (A80) (A81) (A82) (A83)
LX9 J0JBD0000032: A13 A14 A15 A16 → (A84) (A85) (A86) (A87)
LX10 J0JBD0000032: A17 A18 A19 A20 → (A88) (A89) (A90) (A91)
LX12 J0JBD0000032: (A92) (A93) (A94) (A95)

CL56004 CL56005 CL56006

IC11
C3ABPG000063
(64M SD RAM)
VSS DQ15 VSSQ DQ14 DQ13 VCCQ DQ12 DQ11 VSSQ DQ10 DQ9 DQ8 VCCQ DQ8 VSS NC UDQM CLK CKE NC A12 A11 A9 A8 A7 A6 A5 A4 VSS
VCC DQ0 VCCQ DQ1 DQ2 VSSQ DQ3 DQ4 VCCQ DQ5 DQ6 VSSQ DQ7 VCC LDQM /WE /CAS /RAS /CS A13 A12 A10 A0 A1 A2 A3 VCC

IC13
PQ07VZ012ZP
(SWITCHING REG 2.5V)
1 3.3, 2 3.3, 3 2.0, 4 1.6, 5
C95 1, R111 680, R112 1K, C94 1

IC15
uPC2933
(3.3V RGB)
IN GND OUT
5.0, 3.3
C110 1, C111 1

R49 0, C77 0.1, R23 0, C56 4V220, FL10 F1H0J1050019, C58 0.1, C62 0.1

LB114 J0JCC0000188
LB95 J0JCC0000188
LB81 J0JCC0000188
LB97 J0JCC0000188
CL56001
CL56007

IC10
C3FBLD000083
(8M FRASH ROM)
A16 BYTE VSS DQ15/A-1 DQ7 DQ14 DQ6 DQ13 DQ5 DQ12 DQ4 VCC DQ11 DQ3 DQ10 DQ2 DQ9 DQ1 DQ8 DQ0 OE VSS CE A0
A15 A14 A13 A12 A11 A10 A9 A8 A19 NC WE RESET VPP WP RY/BY A18 A17 A7 A6 A5 A4 A3 A2 A1

CL56027 CL56011
CL56026 CL56025 CL56024 CL56023 CL56022 CL56021 CL56020 CL56019 CL56030
CL56012 CL56013 CL56014 CL56015 CL56016 CL56017 CL56018 CL56028 CL56029 CL56010
C25 0.1, R43 0
C31 0.1, Q2 2SB1462 (ROM WRITE:ON), R8 2200
R11 10K, C105 1

IC16
C0EBE0000174
(SYSTEM POWER SUPPLY)
RESET CE GND V OUT
S.CLOCK CD CTW VDD
3.1, 3.1, 3.3
0.2, 0.1, 0.1
C116 4700P, C117 0.047, C115 1

FL9 F1H0J1050019 — R38 0, C57 0.1, C59 0.1
FL8 F1H0J1050019 — C55 6V47, C61 0.1, C64 0.1
FL7 F1H0J1050019 — R84 0, C71 6V47, C65 0.1, C66 0.1, C68 0.1, C79 0.1
FL6 F1H0J1050019 — R21 0, C70 4V220, C67 0.1, C75 0.1

(A99) (A2) (B22) (B2) (B21) (B24) (B25) (B26) (B23) (B27) (B30) (B15)

B-LINE

NV-APD1E/B/EN
CONTROL SECTION IN MAIN SCHEMATIC DIAGRAM

NV-APD1E/B/EN
CONTROL SECTION IN MAIN SCHEMATIC DIAGRAM